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**Invited Speech: Do the Things Right, and Earlier –Project “Shift-Left” with Verification Continuum**



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**About the Speaker**

Thomas Li is the Director of Verification Business Development for Synopsys Inc. He has almost 20 years experiences in semiconductor industry, especially in IC design and verification area. Prior to Synopsys, he had been working with multiple companies in EDA industry including SpringSoft, Mentor Graphics with engineering, marketing and management positions. Mr. Li received a Master of Science degree in Information Engineering from the National Chung-Cheng University, Taiwan.

**Abstract**

For the highly time-pressured modern electronics market, new designs have to be verified from multiple aspects including hardware, software, power...etc. lengthy and unpredictable verification process is one of the most important factors impacts project schedule and time to market (profit). Verification methodology and tool chain need to be reviewed and re-write significantly. As the result, the “shift-left” of the bug discovery and fix process with a comprehensive, tightly integrated verification platform with unified compiler, unified debug interface and unified database is mandatory. This presentation will give introduction to the proposed flow and the case studies from real world cases.